Reg. No:

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)
MCA I Year I Semester Supplementary Examinations November-2021
COMPUTER ORGANIZATION

Time: 3 hours   Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

1	Explain about arithmetic operations on floating point numbers with its neat sketch?	L5	12M

OR

2	Using K- map simplify the Boolean function $F(w, x, y, z)=\sum(0,1,2,4,6,8,9,12,13,14)$.	L4	12M

UNIT-II

3	Clearly explain Hardwired Control with the help of a neat diagram.	L2	12M

OR

4	List and explain different types of Shift Register Operations.	L4	12M

UNIT-III

5	a	What is an interrupt? Explain about simultaneous request handling by the processor.	L1	6M
	b	Explain about process control instructions?	L2	6M

OR

6	Discuss about Arithmetic instructions in detail with a neat diagram.	L6	12M

UNIT-IV

7	Clearly explain about modes of transfer and it types.	L2	12M

OR

8	a	Explain about Programmed I/O.	L6	6M
	b	Explain about Interrupt-initiated I/O.	L6	6M

UNIT-V

9	a	Explain about vector processing.	L2	6M
	b	Explain about Array processors.	L2	6M

OR

10	Explain briefly about Arithmetic pipeline with a neat diagram.	L2	12M

*** END ***